

Electronic Acknowledgement Receipt

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Application Number:	10813837
International Application Number:	
Confirmation Number:	6047
Title of Invention:	VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA EFFECT OF A CMOS BIPOLAR TRANSISTOR
First Named Inventor/Applicant Name:	Akhil K. Garlapati
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Change of Address	026-0044_COA_20080806.pdf	93270 d0540efc951e30b6ccce9dd96e8d99f64 fbffd9f	no	1

Warnings:

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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